

**REMARKS**

At the outset, the Examiner is thanked for the thorough review and consideration of the subject application. The Non-Final Office Action of February 3, 2003 has been received and contents carefully reviewed.

In the Non-Final Office Action, the Examiner objected to claim 19 due to a typographical informality; rejected claim 19 under 35 U.S.C. § 112, second paragraph as containing a limitation lacking sufficient antecedent basis; rejected claims 1-12, 22, and 23 under 35 U.S.C. § 103(a) as being unpatentable over Gu et al. (U.S. Pat. No. 6,359,672); rejected claims 33 and 15-21 under 35 U.S.C. § 103(a) as being unpatentable over Gu et al. in view of Yamazaki (U.S. Pat. No. 5,463,438); and objected to claim 14 as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims. The rejections and objections of these claims is traversed and reconsideration of the claims is respectfully requested in view of the following remarks.

The Examiner is thanked for the indication of allowable subject matter in claim 14.

With respect to the objection of claim 19, and to the rejection of claim 19 under 35 U.S.C. § 112, second paragraph as containing a limitation lacking sufficient antecedent basis, Applicants respectfully submit the above amendment to claim 19 renders the aforementioned objection and rejection of claim 19 moot.

The rejection of claims 1-12, 22, and 23 under 35 U.S.C. § 103(a) as being unpatentable over Gu et al. is traversed and reconsideration is respectfully requested.

Independent claim 1 is allowable over the cited art in that claim 1 recites a combination of elements including, for example, "forming... the data line on a transparent

substrate; ...forming the organic insulating film on the transparent substrate ... the organic insulating film being provide on the data line.” None of the cited references including Gu et al., singly or in combination, teaches or suggest at least this feature of the claimed invention. Accordingly, Applicant respectfully submits that independent claim 1 and claims 2-6, which depend therefrom are allowable over the cited references.

Independent claim 7 is allowable over the cited art in that claim 7 recites a combination of elements including, for example, “a data line; an organic insulating film formed on the... the data line” None of the cited references including Gu et al., singly or in combination, teaches or suggest at least this feature of the claimed invention. Accordingly, Applicant respectfully submits that independent claim 7 and claims 8-12, which depend therefrom are allowable over the cited references.

Independent claim 22 is allowable over the cited art in that claim 22 recites a combination of elements including, for example, “a thin film transistor formed at an intersection between a gate line and a data line, and a pixel electrode connected to a source electrode of the thin film transistor and overlapped with at least one of the gate line and the data line with having an organic insulating film therebetween and being provided on the data line.” None of the cited references including Gu et al., singly or in combination, teaches or suggest at least this feature of the claimed invention. Accordingly, Applicant respectfully submits that independent claim 22 and claim 23, which depends therefrom are allowable over the cited references.

The rejection of claims 13 and 15-21 under 35 U.S.C. § 103(a) as being unpatentable over Gu et al. in view of Yamazaki is traversed and reconsideration is respectfully requested.

Independent claim 13 is allowable over the cited art in that claim 13 recites a combination of elements including, for example, "a thin film transistor formed at an intersection between a gate line and a data line, and a pixel electrode connected to a source electrode of the thin film transistor and overlapped with at least one of the gate line and the data line with having an organic insulating film therebetween and being provided on the data line." None of the cited references including Gu et al. or Yamazaki, singly or in combination, teaches or suggest at least this feature of the claimed invention. Accordingly, Applicant respectfully submits that independent claim 13 and claims 14-21, which depends therefrom are allowable over the cited references.

Applicants believe the foregoing amendments place the application in condition for allowance and early, favorable action is respectfully solicited. Should the Examiner deem that a telephone conference would further the prosecution of this application, the Examiner is invited to call the undersigned attorney at (202) 496-7500.

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If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136. Please credit any overpayment to deposit Account No. 50-0911.

Respectfully submitted,

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**MARKED-UP VERSION OF THE AMENDED CLAIMS**

1. (AMENDED) A method of fabricating a liquid crystal display device including a thin film transistor formed at an intersection between a gate line and a data line, and a pixel electrode connected to a source electrode of the thin film transistor and overlapped with at least one of the gate line and the data line with having an organic insulating film therebetween, said method comprising:

forming the thin film transistor, the gate line and the data line on a transparent substrate;

forming the organic insulating film on the transparent substrate to a thickness of between 0.8 $\mu$ m and 1.5 $\mu$ m, the organic insulating film being provided on the data line; and

forming the pixel electrode on the organic insulating film so as to be overlapped, by a predetermined area, with at least one of the gate line and the data line.

13. (AMENDED) A liquid crystal display device including a thin film transistor formed at an intersection between a gate line and a data line, and a pixel electrode connected to a source electrode of the thin film transistor and overlapped with at least one of the gate line and the data line with having an organic insulating film therebetween and being provided on the data line, wherein a thickness and a dielectric constant of the organic insulating film are selected such that a signal delay is less than 10  $\mu$ sec for each of the gate lines and the data line.

19. (AMENDED) The liquid crystal display device according to claim 13, wherein the dielectric constant of the organic insulating film is less than 3.0.

22. (AMENDED) A liquid crystal display device including a thin film transistor formed at an intersection between a gate line and a data line, and a pixel electrode connected to a source electrode of the thin film transistor and overlapped with at least one of the gate line and the data line with having an organic insulating film therebetween and being provided on the data line, wherein a thickness of the organic insulating film is between 0.8 $\mu$ m and 1.5 $\mu$ m.